



15.32/5685

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:)
KOMIYAMA et al.)
Serial No. 09/776,528)
Filed: February 4, 2001)
For: METHODS FOR MANUFACTURING)
SEMICONDUCTOR CHIPS, METHODS FOR)
MANUFACTURING SEMICONDUCTOR)
DEVICES, SEMICONDUCTOR CHIPS,)
SEMICONDUCTOR DEVICES, CONNECTION)
SUBSTRATES AND ELECTRONIC DEVICES)

Group Art Unit: 2814

Examiner: Ha, Nathan W.

PETITION FOR EXTENSION
OF TIME

Mail Stop RCE
Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In accordance with Rule 1.136, the applicant respectfully petitions the Commissioner for a one month extension of time extending to January 26, 2004, the period for response to the Office Action dated September 26, 2003. The \$110 fee and the responsive papers are attached. If it should be determined that a longer extension of time is required to prevent this application from becoming abandoned, or for any other reason an insufficient fee has been paid, please charge any insufficiency to Deposit Account No. 50-0585. A duplicate copy of this petition is enclosed.

Respectfully submitted,

Alan S. Raynes
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Dated: January 23, 2004

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CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Mail Stop RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on January 23, 2004.

Alan S. Raynes
Alan S. Raynes
January 23, 2004
Date